

ABSTRACT OF THE DISCLOSURE

[0049] A computing system having a processor with a data/control bus interface. A data/control bus implements one or more device communication channels. A data memory 5 is coupled to the processor and a mass storage device having an interface for communicating mass storage transactions is provided. A controller having a memory interface is coupled to the data memory and a mass storage interface coupled to the mass storage device's interface 10 and operable to conduct mass storage transactions between the data memory and the mass storage device.